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INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*	Application Number	09/844,673
	Filing Date	April 30, 2001
	First Named Inventor	THEKKATH
	Group Art Unit	~~2122~~ 2192
	Examiner Name	Rutten, James D.
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T[4]
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OTHER – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
JDR	D1	Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, *1990 ACM*, pp. 37-47.	
JDR	D2	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, *ACM Computing Surveys*, Vol. 29, No. 2, June 1997, pp. 128-170.	
JDR	D3	Eric ROTENBERG, et al.,"Trace Processors," University of Wisconsin, 1997 *IEEE Service Center*, 12 pp.	
JDR	D4	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©*1999 ACM 1-58113-083*, pp. 214, 215.	
JDR	D5	Darren JONES, "Opal Coprocessor Interface," *MIP Propietary/Confidential*, June 4, 1999, pp. 1-18.	
JDR	D6	Darren JONES, *MIPSS64™ 5KC™ Processor Cores User's Manual*, Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.	
JDR	D7	*MIPSS64™ 5KC™ Processor Cores User's Manual*, Rev. 1.0.1., 1999, pp. 247-276.	

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